

In the Claims

Please amend the claims as follows:

1. (currently amended) A computer core comprising at least one interface signal carrier that is configurable, at compilation, such that the at least one interface signal carrier can be selectively present.
2. (currently amended) The computer core as set forth in claim 1, wherein at least one interface signal capable of being carried by said at least one interface signal carrier is further configured to support different levels of functionality.
- c1 3. (currently amended) The computer core as set forth in claim 2, wherein a signal carrier width of the at least one interface signal carrier is configurable to support different signal widths.
4. (currently amended) A computer core comprising at least one interface signal carrier that is configurable, at compilation, such that the at least one interface signal carrier can be configured to support different levels of functionality.
5. (currently amended) The computer core as set forth in claim 4, wherein a signal carrier width of the at least one interface signal carrier is configurable to support different signal widths.
6. (currently amended) A computer core comprising at least one interface signal carrier, a signal width of the at least one interface signal carrier being configurable at compilation to support different signal widths.
7. (currently amended) A method for generating at compilation a computer core interface, the method comprising:
 - providing configurable source code representative of the computer core interface and identifying parameters of the computer core interface;
 - defining configuration parameters of the computer core interface; and
 - generating a computer core from the configurable source code representative of the computer core interface comprising an interface comprising the identified parameters of the

computer core interface configurable in accordance with the defined configuration parameters of the computer core interface.

C1 8. (currently amended) The method as set forth in claim 7, wherein at least one of the configuration parameters of the computer core interface ~~is~~ are selected from the group ~~comprising~~ consisting of parameters that define whether ~~the~~ a signal carrier is present in the computer core interface, define different levels of functionality of the computer core interface, and define a signal width of at ~~least~~ least one computer core interface signal.

9. (currently amended) A method for generating at compilation a core with a configured interface, the method comprising:

implementing the core as configurable source code utilizing at least one defined parameter of an interface of the core;

selecting at least one configuration option of the at least one defined parameter of the interface of the core;

generating ~~a~~ the core comprising ~~a~~ the at least one defined parameter of the ~~core~~ interface of the core that operates in accordance with the selected interface comprising the selected at least one configuration option.

10. (currently amended) The method as set forth in claim 9, wherein selecting at least one configuration option of the at least one defined parameter of the interface of the core is performed through a graphical user interface.

11. (currently amended) The method as set forth in claim 9, wherein selecting at least one configuration option of the at least one defined parameter of the interface of the core is performed by deriving the at least one configuration option of the at least one defined parameter of the interface of the core ~~configuration options~~ from ~~the~~ a ~~configurations~~ configuration option of another core.